A BIOLOGICALLY INSPIRED HARDWARE MODULE FOR EMBEDDED AUDITORY SIGNAL PROCESSING APPLICATIONS

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- Keywords: Bio-inspired system, Auditory subsystems, Acoustic signal processing, Digital filter, FPGA, System on chip.
- Abstract: This paper presents a fully parameterised and highly scalable design prototype of FPGA (field programmable gate array) implementation of a biologically inspired auditory signal processing system. The system has been captured and simulated using system-level integrated design tools, namely, System GeneratorTM and AccelDSPTM both from XilinxTM. The implemented hardware auditory periphery model consists of two sub-models—the Patterson's Gammatone filter bank and the Meddis' inner hair cell. The prototype has been successfully ported onto a VirtexTM –II Pro FPGA. Ultimately, it can be used as a front-end apparatus in a variety of embedded auditory signal processing applications.

1 INTRODUCTION

The human peripheral auditory system, as illustrated in Figure 1, consists of the outer, middle and inner ear. The inner ear, or the cochlea, is a coiled tube filled with fluid. The sound vibration is transmitted to the fluid, and then to the basilar membrane of the cochlea. The stiffness of the basilar membrane decreases exponentially along the length of the cochlea. This makes the basilar membrane act like a frequency analyser with the base part (near the oval window) responding to high frequencies, and the apical part (the far end) responding to lower frequencies. The sensory cells to detect the frequencies are the hair cells attached to the basilar membrane. There are three rows of outer hair cells (OHC) and one row of inner hair cells (IHC). For human, there are approximately 12,000 OHCs and 3,500 IHCs (Truax, 1999). The movement of the OHCs and the basilar membrane is conveyed to the IHCs and causes a depolarisation, which in turn results in a receptor potential. Thus, IHCs release neurotransmitters, whose concentration change gives rise to nerve spikes into the auditory nerve.

The human auditory system deals with a wide range of everyday real life applications such as pitch detection, sound localisation and speech recognition, just to name few. It does the job extremely well, and far better than the current acoustic sensor technology based systems. The auditory system consists of the auditory periphery, as the front end sensing apparatus, and includes different other regions of the brain up to



Figure 1: The human ear (Truax, 1999).

the auditory cortex. The auditory periphery 'transduces' acoustical data into train of spikes for more high level neuronal processing. Based on this, many researchers and engineers believe that modelling and implementation of an artificial auditory subsystems, especially the cochlea, will yield better performance in acoustic signal processing. The reported electronic cochlea implementations fall into two categories.

One is the task-oriented engineering approach, which treats the whole cochlea as filters in order to obtain the time/frequency information that can be used for different kinds of post-processing. These are the majority of the reported implementations, including the first electronic cochlea proposed by Lyon and Meads (Lyon and Mead, 1988), some recent FPGA implementations (Mishra and Hubbard, 2002), (Leong et al., 2003), and (Wong and Leong, 2006).

The other category is the research-oriented signal

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A BIOLOGICALLY INSPIRED HARDWARE MODULE FOR EMBEDDED AUDITORY SIGNAL PROCESSING APPLICATIONS. In Proceedings of the International Conference on Bio-inspired Systems and Signal Processing, pages 382-387 DOI: 10.5220/0001554703820387 Copyright © SciTePress

processing approach, which analyses each stage of the biological acoustic signal processing in the auditory neurological system. There is less work in this category, and most of them focus on the hair cells and auditory nerve. Lim reported a pitch detection system (Lim et al., 1997) based on the Meddis' inner hair cell model (Meddis, 1986), then Jones improved it to a four-stage pitch extraction system (Jones et al., 2000). A spike-based sound localisation system was implemented by Ponca (Ponca and Schauer, 2001). There was an analogue hair cell model implemented and then improved by van Schaik (van Schaik and Meddis, 1999), (van Schaik, 2003).

The prototype implementation of the auditory subsystem in this paper belongs to the second category. It models a part of the signal processing of the human cochlea, by connecting two widely accepted models, the Patterson's Gammatone filter bank (GFB) (Patterson et al., 1992) and the Meddis' inner hair cell (IHC) (Meddis, 1986). Compared to other existing work, the proposed hardware implementation is fully parameterised and highly scalable. It provides a good platform for further research, and can be developed at the front-end of an embedded auditory signal processing system.

2 PATTERSON'S GAMMATONE FILTER BANK

The GFB proposed by Patterson (Patterson et al., 1992) is a set of parallel Gammatone filters, each of which responds to a specific frequency range. The Gammatone filter is a bandpass filter with gamma distribution well known in statistics. It describes the impulse response of a cat's cochlea, which is very similar to the human cochlea. The GFB provides a reasonable trade-off between accuracy in simulating the basilar membrane motion and computational load. Some improved models have been developed based on the original work, however, due to the increased hardware computation burden, the original model is adapted here. The impulse response of a Gammatone filter is:

$$h(t) = At^{N-1} e^{-2\pi bt} \cos(2\pi f_c t + \varphi) \qquad (t \ge 0, N \ge 1)$$
(1)

Where A is an arbitrary factor that is typically used to normalise the peak magnitude to unity; N is the filter order; b is a parameter that determines the duration of the impulse response and thus the filters bandwidth, f_c is the centre frequency, and φ is the phase of the tone. Slaney developed a digital version of the GFB (Slaney, 1993), then implemented it in his famous MatlabTM "Auditory Toolbox" (Slaney, 1998). Each digital Gammatone filter consists of four second-order sections (SOS) or Infinite Impulse Response (IIR) filters, as illustrated in Figure 2. The general Z transfer function for each IIR filter is:

$$H(z) = \frac{A_0 + A_1 z^{-1} + A_2 z^{-2}}{1 + B_1 z^{-1} + B_2 z^{-2}}$$
(2)



Figure 2: Slaney's digital Gammatone filter bank.

3 MEDDIS' INNER HAIR CELL MODEL

Meddis introduced the first hair cell model (Meddis, 1986), which describes the transduction between IHCs and auditory nerves in a manner quite close to physiology by modelling both the short term and long term adaptation characteristics of the IHCs. Just like the case of the GFB model, the Meddis' IHC model has also been improved to adapt new findings in biology, however, for simplicity, the original model is chosen for this prototype implementation. The Meddis' IHC model can be described by a set of four nonlinear equations (Meddis, 1986).

$$k(t) = \begin{cases} \frac{g(s(t)+A)}{s(t)+A+B} & \text{for } s(t)+A > 0\\ 0 & \text{for } s(t)+A \le 0 \end{cases}$$
(3)

$$\frac{dq}{dt} = y(1-q(t)) + rc(t) - k(t)q(t)$$
(4)

$$\frac{dc}{dt} = k(t)q(t) - lc(t) - rc(t)$$
(5)

$$P(e) = hc(t)dt \tag{6}$$

Where k(t) is the permeability; s(t) is the instantaneous amplitude; q(t) is the cell transmitter level;

P(e) is the spike probability; A, B, g, y, l, x, r and m are constants based on statistics; and h is the proportionality factor, which can be set to different values.

The underlying structure of the model is illustrated in Figure 3 (Meddis, 1986). It is worth noting that there is also a MatlabTM implementation of the Meddis' IHC model in Slaney's "Auditory Toolbox" (Slaney, 1998), which provides a reference for this design prototype.



Figure 3: The Meddis' inner hair cell model (Meddis, 1986).

4 SYSTEM IMPLEMENTATION

4.1 System Architecture

The proposed system architecture, as illustrated in Figure 4, consists of a GFB (a set of Gammatone filters) that mimics the behaviour of the basilar membrane, interfaced in a parallel fashion to the Meddis' IHC module through a bank of buffers. Each Gammatone filter is combined with a single Meddis' IHC to process for a particular range of frequencies (double-lined circle in Figure 4). The GFB (basilar membrane module) processes the incoming signal using parallel channels for different frequency ranges, and generates outputs that represent the vibration displacements along different parts of the biological basilar membrane. The Meddis' IHC module calculates the probability rate of neural spikes (spikes/second) corresponding to each output generated by the GFB module. A channel structure consisting of a Gammatone filter, a buffer and an IHC (double-lined circle in Figure 4) could be reconfigured to implement any of the system channels (1, 2, 3, ..., n, ..., N) through parameterisation. The system is also scalable, which means that an arbitrary number of channels can be generated, again through parameterisation. Simulation can be carried out either in software or hardware, however, for this prototyping stage; software simulation is preferred.

The specifications of the complete model are as follows. First, the "audible" frequency range of the basilar membrane module has to cover the human auditory range—from 200 Hz to 20 kHz (Truax, 1999).

A direct consequence is that the sampling frequency of the system sets to 44 kHz to allow a reasonable discrete representation. Second, perhaps a little arbitrarily, the number of channels is set to 20. Although the number of GFB channels should be the same as the number of IHCs in the cochlea (3,500), a compromise has to be made because of the constraints on hardware. Finally, the system must operate in realtime.

The general design methodology relies predominantly on IP-based blocks to model DSP primitives such as adders and multipliers using signed fixedpoint bit serial arithmetic. Appropriate number representation, quantisation and overflow handling of the signals in the system are keys for a successful implementation. As such, the system input was coded as 14-bit signed fixed-point numbers, with 12 fractional bits. The output of the basilar membrane module was reduced to 10 bits with 8 fraction bits to relieve the computation load for the Meddis inner hair cell module. The output of the system was set as 14-bit with 12 fractional bits, in a similar fashion to the input.



Figure 4: Structure for the whole model and the implemented channel.

4.2 Basilar Membrane Module

For the proposed design prototype, only one channel of the GFB was implemented using System GeneratorTM (Xilinx, 2008b). A channel or a Gammatone filter consists of four second-order IIR filters (SOS), each of which is implemented in the direct form structure, as illustrated in Figure 5. The calculations of the SOSs' coefficients are achieved by using Slaney's "Auditory Toolbox". There is no A_2 forward path as indicated in the transfer function of equation 2, simply because the coefficient A_2 is always zero for all the SOSs of any channel. The numerator coefficients (A_0 and A_1) of each SOS are scaled by $\sqrt[4]{\text{total gain}}$, resulting in the Gammatone filter to be scaled by the total gain which is calculated by the toolbox. This scaling narrows the dynamic range of the intermediate signals, and results in a reduced word length for the adders and multipliers. Table 1 presents an example of the calculated coefficients for the first channel of the GFB, where the total gain for this channel is 2.90294×10^{16} . Consequently, all the coefficients A_0 and A_1 in the table are scaled by 1.30529×10^4 . It is worth noting that only the coefficient A_1 of the a SOS differ from those of the other 3 SOSs in the same channel, which makes a hardware optimisation possible for a single SOS.



Figure 5: A second-order section of the Gammatone filter.

	Table 1:	Coefficients	of the	IIR filters	in channel	1
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IIR	A_0	A_1	B_1	B_2
SOS 1	0.29665	-0.10187	1.26532	0.56372
SOS 2	0.29665	0.47724	1.26532	0.56372
SOS 3	0.29665	0.13800	1.26532	0.56372
SOS 4	0.29665	0.23736	1.26532	0.56372

4.3 Meddis Inner Hair Cell Module

Slaney's MatlabTM implementation (Slaney, 1998) of the Meddis' IHC model must be revised prior to be synthesised by AccelDSPTM (Xilinx, 2008a). This is not only because of the constraint requirements imposed by the AccelDSPTM software, but also the demand for a real-time DSP system. The revised implementation processes the input signal in a fixed-point, bit-serial fashion, which leads to an unavoidable error compared to the original floating-point model, as illustrated in Figure 6. The investigation into the reported hardware implementation ((Lim et al., 1997),(Jones et al., 2000)) highlights this error as well. In reality, this is not a critical issue since the exact numerical values of the probability spike rate output are not essential for the spike generation (could be scaled up using the *h* coefficient in equation 6), however, the half-wave rectification, the saturation and the adaptation (both long term and short term) characteristics of the output are in return quite important (Jones et al., 2000). The Meddis IHC module was generated based on this revised model using AccelDSPTM. Figure 6 gives a comparison between the generated fixed-point model and the original floating-point model using a 1 kHz sine wave input.



Figure 6: From top to bottom: outputs of the fixed-point Meddis' IHC model, the floating-point Meddis' IHC model and the error, with 1 kHz sine wave input.

4.4 Simulation and Synthesis Results

The test bench illustrated in Figure 7 was built to compare the simulation results of the original MatlabTM floating-point model, the System GeneratorTM fixedpoint model and the FPGA hardware implementation model, however, the simulation here are only carried out in software at this prototyping stage. The input signal is generated by SimulinkTM blocks and can also be imported from files or even real-time external events. By an initialisation script, the total number of channels of the auditory subsystem can be set to an arbitrary number, in this case, 20, and the fixedpoint model can be configured as anyone of the channels. The simulation results, shown in Figure 8, depicts the outputs of the 5_{th} channel under consideration for a step and then a sine wave input functions respectively. The hardware model generates closely matched outputs comparing with that of the software implementation in the simulation. The synthesis report illustrated in Table 2 indicates that the hardware utilisation is quite low (7%), except for the multipliers (32%), but the design can run in real-time. It also implies that only 3 channels can be implemented in parallel for this FPGA chip.

Table 2: Synthesis report of the first channel of the auditory subsystem.

Target Device	XC2VP30	
Synthesis Tool	XST v10.1.01	
Used Slices	993	7%
Used Slice Flip Flops	827	3%
Used 4 input LUTs	2,574	9%
Used RAMB16s	2	1%
Used MULT18X18s	44	32%
Max Frequency	17.505 MHz	

5 CONCLUSIONS AND FUTURE WORK

The paper presents the design and FPGA implementation of a bio-inspired hardware module that can be used as a front end apparatus in a variety of embedded auditory signal processing applications. The implementation consists of two sub-modules. Patterson's GFB and Meddis' IHC, linked together to mimic the behaviour of a single frequency channel of the auditory periphery. The proposed design is fully parameterised and highly scalable. The design prototype has been captured and then simulated using two integrated tools, System GeneratorTM and AccelDSPTM both from XilinxTM. The prototype works as expected and the design process is much faster than the traditional hardware description language (HDL) design flow. The resulting hardware structure was too large to accommodate a 20-channel parallel auditory subsystem; therefore, a time-shared multiplexing scheme is envisaged for future implementations. More optimisation can be achieved for the filter modules to improve the system performance and reduce the number of multipliers. The ultimate goal is to build a complete bio-inspired system that models the signal processing of the whole human auditory system.

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Figure 7: Test bench for the simulation of the prototype implementation of the auditory subsystem.



Figure 8: Simulation results for the 5_{th} channel. (a) the output of the basilar membrane module with step input; (b) the output of the basilar membrane module with 1 kHz sine input; (c) the output of the Meddis IHC module with step input; (d) the output of the Meddis IHC module with 1 kHz sine input. For each sub-figure, the output of the fixed-point hardware model is at the top, the output of the floating-point software module is in the middle, and the calculated error is at the bottom. The X-axis represents the simulation time, and the Y-axis represents the amplitude of the output.